

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Master of Technology (Computer Science and Engineering)
Choice Based Credit System (CBCS)-2019-20

SEMESTER - IV

S. No.	COURSE CODE	COURSE NAME	Examination Scheme					Teaching Scheme/Week			CREDITS	TOTAL MARKS
			THEORY			PRACTICAL		Th	T	P		
			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)					
1	MTCS401	Dissertation Phase-II	0	0	0	300	200	0	0	32	16	500

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.


 Chairperson
 Board of Studies
 (Computer Science & Engineering,
 Information Technology & Computer Applications)
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


 Joint Registrar
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore